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			U.S. PATENT DOCUMENTS			
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EXAMINER INITIAL		OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
	-	Eles, P., et al., "Scheduling of Conditional Process Graphs for the Synthesis of Embedded Systems" Proc. 1st ACM/IEEE Design and Test in Europe Conf., Paris France, pp. 132-138, February 1998.
MI		Ha, S., "Compile-Time Scheduling of Dynamic Constructs in Dataflow Program Graphs", IEEE Trans. on Computers, Vol. C-47, No. 7, pp. 768-778, July 1997.
A S	+	Proang, P., "Scheduling of DSP Programs onto Multiprocessors for Maximum Throughput", IEEE Trans. on Signal Processing, Vol. 41, No. 6, June 1993.
	-	Hong, I, et al., "On-Line Scheduling of Hard Real-Time Tasks on Variable Voltage", IEEE International Conf. On Comp. Aided Design, Santa Clara, CA, pp. 653-656, November 1998.
200		Li, Y., et al., "Scheduling and Allocation of Single-Chip Multiprocessors for Multimedia", IEEE Wsh. On Signal Processing Systems (SIPS), Leicester, UK, pp. 97-106, November 1997.
	ļ.	Thoen, F., et al., "Modeling, Verification and Exploration of Task-Level Concurrency in Real-Time Embedded Systems", ISBN 0-7923-7737-0, Kluwer Acad. Publ., Boston, 1999.
		Tuck, B., "Raise your Sights to the System Level - Design Report 1997 Paris Forum", Computer Design, pp. 53-74, June 1997.

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